

Shaheed Rajguru College of Applied Sciences for Women

B.Sc. (H) Electronics – V semester

wef: 20-07-2021

Time	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30	
Day	1	2	3	4		5	6	7	8	
Monday	Digital Signal Processing Mr. Deepak Jaiswal	EMT Dr. Neha Katyal	Numerical Analysis Ms. Venika Gupta	Microprocessor & Microcontrollers Ms. Preeti Singhal	L U N C H B R E A K	EMT Practical Group - B Ms. Surbhi Aggarwal		Microprocessor & Microcontrollers Practical Group - B Ms. Preeti Singhal		
						Numerical Analysis Practical Ms. Venika Gupta Group – D		EMT Practical Dr. Neha Katyal Group – D		
Tuesday	EMT Dr. Neha Katyal	Basic VLSI Design Dr. Manoj Kumar	Numerical Analysis Ms. Venika Gupta	Microprocessor & Microcontrollers Ms. Preeti Singhal		EMT Practical Group - B Ms. Surbhi Aggarwal				
		Digital Signal Processing Mr. Deepak Jaiswal				Microprocessor & Microcontrollers Practical Group - D Ms Preeti Singhal		Numerical Analysis Practical Group - D Ms. Venika Gupta		
Wednesday		Basic VLSI Design Group - B Dr. Manoj Kumar	Numerical Analysis Ms. Venika Gupta	Microprocessor & Microcontrollers Ms. Preeti Singhal		Numerical Analysis Practical Group – B Ms. Venika Gupta		Basic VLSI Design Practical Group - B Dr. Manoj Kumar		
						Digital Signal Processing Practical Group - D Mr. Deepak Jaiswal				
Thursday		EMT Ms. Surbhi Aggarwal	Numerical Analysis Ms. Venika Gupta	Basic VLSI Design Dr. Manoj Kumar		L U N C H B R E A K	Numerical Analysis Practical Group – B Ms. Venika Gupta		Basic VLSI Design Practical Group - B Dr. Manoj Kumar	
				Digital Signal Processing Mr. Deepak Jaiswal			Microprocessor & Microcontrollers Practical Group – D Ms Preeti Singhal		Digital Signal Processing Practical Group - D Mr. Deepak Jaiswal	
Friday		Basic VLSI Design Dr. Manoj Kumar	Microprocessor & Microcontrollers Ms. Preeti Singhal	EMT Ms. Surbhi Aggarwal	Microprocessor & Microcontrollers Practical Group – B Ms Preeti Singhal					
		Digital Signal Processing Mr. Deepak Jaiswal			EMT Practical Group – D Dr. Neha Katyal					

No change in timetable without the permission of Principal

Dr. Punita Saxena (Convenor)

Shaheed Rajguru College of Applied Sciences for Women

B.Sc. (H) Electronics – III Semester

wef : 16-08-2021

Time	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30
Day	1	2	3	4		5	6	7	8
Monday	GE III	GE III	Electronic Circuit Ms. Sonia Ahlawat	C Programming Ms. Monika Tyagi	L U N C H	Electronic Circuit Practical Group – 3A Ms. Sonia Ahlawat		C Programming Practical Group – 3A Ms. Monika Tyagi	
						C Programming Practical Group – 3B Ms. Monika Tyagi		Electronic Circuit Practical Group – 3B Ms. Sonia Ahlawat	
Tuesday	GE III	Electronic Circuit Ms. Sonia Ahlawat	Digital Electronics Dr. Neha Katyal	C Programming Ms. Monika Tyagi	B R E A K	Electronic Circuit Practical Group – 3A Ms. Sonia Ahlawat		C Programming Practical Group – 3A Ms. Monika Tyagi	
						C Programming Practical Group – 3B Ms. Monika Tyagi		Electronic Circuit Practical Group – 3B Ms. Sonia Ahlawat	
Wednesday	GE III	Digital Electronics Practical Group – 3A & Group – 3B Dr. Neha Katyal		Digital Electronics Dr. Neha Katyal	B R E A K	Data Science Practical Group – 3A & Group – 3B Ms. Monika Tyagi & Ms. Sonia Ahlawat		Data Science Practical Group – 3A & Group – 3B Ms. Monika Tyagi & Ms. Sonia Ahlawat	
Thursday	Electronic Circuit Ms. Sonia Ahlawat	C Programming Ms. Monika Tyagi	Data Science Ms. Monika Tyagi	Digital Electronics Dr. Neha Katyal		Digital Electronics Practical Group – 3A Dr. Neha Katyal		Digital Electronics Practical Group – 3B Dr. Neha Katyal	
Friday	C Programming Ms. Monika Tyagi	Electronic Circuit Ms. Sonia Ahlawat	Digital Electronics Dr. Neha Katyal	Data Science Ms. Sonia Ahlawat	GE III Lab				

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Dr. Punita Saxena (Convenor)

Shaheed Rajguru College of Applied Sciences for Women

B.Sc. (H) Electronics – I Semester

wef: as per the university notification

Time	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30	
Day	1	2	3	4		5	6	7	8	
Monday			Maths FE Ms. Surbhi Aggarwal	GE I	L U N C H B R E A K	Basic Circuit Theory & Network Analysis Mr. Deepak Jaiswal	AECC			
Tuesday		AECC	Maths FE Practical Group 1A Ms. Surbhi Aggarwal	GE I Lab						
			Basic Circuit Theory & Network Analysis Practical Group-1B Mr. Deepak Jaiswal	GE I						
Wednesday	Basic Circuit Theory & Network Analysis Mr. Deepak Jaiswal	GE I	Maths FE Practical Group 1B Ms. Surbhi Aggarwal	GE I (Tut)						
			Basic Circuit Theory & Network Analysis Practical Group-1A Mr. Deepak Jaiswal	Maths FE Ms. Surbhi Aggarwal						
Thursday	Basic Circuit Theory & Network Analysis Mr. Deepak Jaiswal	GE I	Maths FE Practical Group 1A & Group 1B Ms. Surbhi Aggarwal	AECC						
Friday	GE I	Maths FE Ms. Surbhi Aggarwal	Basic Circuit Theory & Network Analysis Practical Group 1A & Group 1B Ms. Venika Gupta	Basic Circuit Theory & Network Analysis Mr. Deepak Jaiswal						
Saturday										

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Dr. Punita Saxena (Convenor)