

SHAHEED RAJGURU COLLEGE OF APPLIED SCIENCES FOR WOMEN

B.Sc. (Hons) Electronics (V Semester)

w.e.f: **1-Aug-2024**

Days	1	2	3	4	Lunch Break	5	6	7	8	
	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30	
MONDAY	VLSI SA D&A Lab	VLSI Practical SA ; NK E Sim - 2		ES MT D&A Lab		GE V(Lab)				
TUESDAY	VLSI SA D&A Lab	EMT Guest D&A Lab	Embedded System Practical MT ; PS E Sim - 1			Telecom VG D&A Lab	EMT NK D&A Lab	GE V Th.	GE V Tut/Lab	GE V Lab
WEDNESDAY	VLSI SA D&A Lab	ES MT D&A Lab	Telecom VG D&A Lab			Telecom Practical VG E Sim - 1	SEC			
			C N/w SG C&N Lab			C N/w Practical SG E Sim - 2				
THURSDAY	SEC		ES MT C&N Lab	EMT NK C&N Lab						
FRIDAY	GE V Th	GE V Th	Telecom VG D&A Lab	EMT NK D&A Lab	EMT Practical NK ; SA E Sim - 2					
			C N/w SG C&N Lab							

No change in Time table is possible without the permission of the Principal

Convenor

Principal

Paper Name	Telecom - Telecommunication Switching Systems and Networks	ES - Embedded System	VLSI - Basic VLSI Design	EMT - Electromagnetics	C N/w - Computer Networks
Faculty	VG - Ms. Venika Gupta	MT - Dr. Monika Tyagi	Ms. Sonia Ahlawat	NK - Dr. Neha Katyal	SG - Ms. Surbhi Gupta
Labs	E Sim 2 - E Simulation Lab - 2	E Sim 1 - E Simulation Lab - 1	E Sim 2 - E Simulation Lab - 2	E Sim 2 - E Simulation Lab - 2	E Sim 2 - E Simulation Lab - 2
Classes	D&A Lab - Digital & Analog Lab	C&N Lab- Circuit & Network Lab			

B.Sc. (Hons) Electronics (III Semester)

w.e.f: **1-Aug-2024**

Days	1	2	3	4	Lunch Break	5	6	7	8
	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30
MONDAY	GE III Th	GE III Th	EM SG C&N Lab	SS Practical (Group 3A) VG E Sim - 2		SS Practical (Group 3A) VG E Sim - 2	Algo MT C&N Lab		
				EM Practical (Group 3B) SG E Sim - 1		EM Practical (Group 3B) SG E Sim - 1			
TUESDAY	VAC	AE II PS C&N Lab	EM SG E Sim - 2	SS VG E Sim - 2		Algo Practical MT E Sim - 2	SEC		
WEDNESDAY	SEC		AE II PS C Lab	SS VG D&A Lab					
					AE II Practical (Group 3B) PS D&A Lab				
THURSDAY	Algo MT C&N Lab	AE II PS C&N Lab	AE II Practical (Group 3A) PS D&A Lab			EM Practical (Group 3A) SG E Sim - 1	VAC		
			SS Practical (Group 3B) VG E Sim - 2						
FRIDAY		EM SG D&A Lab	Algo MT E Sim - 2	SS VG D&A Lab		GE III Lab			
						GE III Th	GE III Tut/Lab	GE III Lab	
SATURDAY									

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Convenor

Principal

Paper Name	SS - Signals and Systems	AE II - Analog Electronics – II	Algo - Algorithm Design and Analysis	EM - Engineering Mathematics
Faculty	VG - Ms. Venika Gupta	PS - Ms. Preeti Singhal	MT - Dr. Monika Tyagi	SG - Ms. Surbhi Gupta
Labs	E Sim 2 - E Simulation Lab - 2	D&A Lab - Digital & Analog Lab	E Sim 1 - E Simulation Lab - 1	E Sim 2 - E Simulation Lab - 2
Classes	C&N Lab- Circuit & Network Lab	D&A Lab - Digital & Analog Lab		

B.Sc. (Hons) Electronics (I Semester)

w.e.f: **29-Aug-2024**

Days	1	2	3	4	Lunch Break	5	6	7	8	
	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30	
MONDAY	VAC	Py Practical MT E Sim - 1		CT NK C&N Lab		Py Guest C&N Lab				
TUESDAY		Py MT E Sim - 1	SD Practical (Group 1A) SA D&A Lab			GE I Lab				
			CT Practical (Group 1 B) NK C&N Lab			GE I Th	GE I Tut/Lab	GE I Lab		
WEDNESDAY		SD SA C&N Lab	Py MT E Sim - 1	CT NK E Sim - 1				VAC		
THURSDAY	GE I Th	GE I Th	CT NK E Sim - 1	SD SA E Sim - 1		CT Practical (Group 1 A) NK C&N Lab		SEC	SEC	
						SD Practical (Group 1B) SA D&A Lab				
FRIDAY	SEC	SEC	SD SA E Sim - 1			Py Practical MT E Sim - 1				
SATURDAY										

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Convenor

Principal

Paper Name	Programming Fundamentals using Python	Semiconductor Devices	Circuit Theory and Network Analysis
Faculty	MT - Dr. Monika Tyagi	SA - Ms. Sonia Ahlawat	NK - Dr. Neha Katyal
Labs	E Sim 1 - E Simulation Lab - 1	D&A Lab - Digital & Analog Lab	C&N Lab- Circuit & Network Lab