

SHAHEED RAJGURU COLLEGE OF APPLIED SCIENCES FOR WOMEN

B.Sc. (Hons). Electronics VII Semester

w.e.f: 01-08-2025

Days	1	2	3	4	Lunch Break	5	6	7	8
	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30
MONDAY									
TUESDAY	GE VII (A) Th	GE VII (A) Th	CMOS NK C & N Lab	INS MT C & N Lab		CS Practical VG E Sim 2			
WEDNESDAY	CMOS Practical NK E Sim 2		INS MT Comm Lab	CS VG Comm Lab		GE VII (B) (Lab)			
THURSDAY		INS MT C & N Lab	CMOS NK C & N Lab	CS VG C & N Lab		GE VII (B) Th	GE VII (B) TUT		
						GE VII(B) LAB			
						GE VII (A)(Lab)			
						GE VII(A) Th	GE VII (A) TUT		
GE VII (A) LAB									
FRIDAY	GE VII(B) Th	GE VII (B) Th	INS MT C & N Lab	CS VG C & N Lab	CMOS NK C & N Lab				
SATURDAY									

No change in Time table is possible without the permission of the Principal

Convenor

Principal

Paper Name	CS - Control Systems		INS - Introduction to Nanoscience		CMOS - CMOS Digital VLSI Design		
Faculty	VG - Ms. Venika Gupta		MT - Dr. Monika Tyagi		NK - Dr. Neha Katyal		
Labs	E Sim 2 - E Simulation Lab - 2		Comm Lab - Communication Lab				
Classes	C&N Lab - Circuit & Network Lab		Comm Lab - Communication Lab				
GE	Internet of Things		Neural Networks		DSE	Introduction to Nanoscience	CMOS Digital VLSI Design

B.Sc. (Hons). Electronics V Semester

w.e.f: **01-08-2025**

Days	1	2	3	4	Lunch Break	5	6	7	8
	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30
MONDAY	EMT NK D & A Lab	ES PS D & A Lab	VLSI SA D & A Lab	CN SG D & A Lab		GE V (LAB)			
			GE V(Th.)	GE V (TUT)					
			GE V (LAB)						
TUESDAY	VLSI Practical (Group - B) SA E Sim 1		VLSI SA Comm Lab	ES PS Comm Lab		SEC			
	CN Practical (Group A) SG E Sim 2								
WEDNESDAY	GE V Th		EMT NK E Sim 2	VLSI SA E Sim 2		CN SG C & N Lab			
THURSDAY	ES Practical (Group A) PS E Sim 2		VLSI Practical (Group A) SA E Sim 1			EMT NK C & N Lab			
	EMT Practical (Group B) NK E Sim 1		CN Practical (Group B) SG E Sim 2						
FRIDAY		ES PS Comm Lab	EMT Practical (Group A) NK E Sim 1			CN SG D & A Lab			
			ES Practical (Group B) PS E Sim 2						
SATURDAY									

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Convenor

Principal

Paper Name	ES - Embedded System		EMT - Electromagnetics	VLSI - Basic VLSI Design		CN - Computer Networks		
Faculty	PS - Ms. Preeti Singhal		NK - Dr. Neha Katyal	SA - Ms. Sonia Ahlawat		SG - Ms. Surbhi Gupta		
Labs	E Sim 2 - E Simulation Lab - 2			E Sim 1 - E Simulation Lab - 1				
Classes	D&A Lab - Digital & Analog Lab			Comm Lab - Communication Lab				
GE	Robotics	DSE	Computer Networks					

B.Sc. (Hons). Electronics III Semester

w.e.f: 01-08-2025

Days	1 9:00-10:00	2 10:00-11:00	3 11:00-12:00	4 12:00-1:00	Lunch Break 1:00-1:30	5 1:30-2:30	6 2:30-3:30	7 3:30-4:30	8 4:30-5:30
MONDAY	GE III Th	GE III Th	EM SG E Sim 2	AE II PS E Sim 2		SS Practical (Group A) VG E Sim 2	VAC		
						AE II Practical (Group B) PS D & A Lab			
TUESDAY		AE II PS Comm Lab	EM Practical (Group A) SG E Sim 1						
			SS Practical (Group B) VG E Sim 2						
WEDNESDAY	VAC		EM Practical (Group B) SG E Sim 1			SS VG Comm Lab			
			AE II Practical (Group A) PS Comm Lab						
THURSDAY		EM SG D & A Lab	SS VG D & A Lab	AE II PS D & A Lab		SEC			
FRIDAY			SS VG Comm Lab	EM SG Comm Lab		GE III Lab			
						GE III Th	GE III (TUT) GE III (LAB)		
SATURDAY									

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Convenor

Principal

Paper Name	SS - Signals and Systems	AE II - Analog Electronics – II	EM - Engineering Mathematics
Faculty	VG - Ms. Venika Gupta	PS - Ms. Preeti Singhal	SG - Ms. Surbhi Gupta
Labs	D&A Lab - Digital & Analog Lab	E Sim 1 - E Simulation Lab - 1	E Sim 2 - E Simulation Lab - 2
Classes	D&A Lab - Digital & Analog Lab	Comm Lab - Communication Lab	
GE	Artificial Intelligence and Machine Learning	SEC	App Development using Flutter

B.Sc. (Hons). Electronics I Semester

w.e.f: 01-08-2025

Days	1 9:00-10:00	2 10:00-11:00	3 11:00-12:00	4 12:00-1:00	Lunch Break 1:00-1:30	5 1:30-2:30	6 2:30-3:30	7 3:30-4:30	8 4:30-5:30
MONDAY	VAC		Py Practical (Group A) MT E Sim 1			SD SA C & N Lab			
			CT Practical (Group B) NK C & N Lab						
TUESDAY		CT NK D & A Lab	PY MT D & A Lab			GE I Lab			
						GE I Th	GE I (TUT)		
WEDNESDAY	PY MT E Sim 1	PY MT E Sim 1	SD SA C & N Lab			SEC			
THURSDAY	GE I Th	GE I Th				SD Practical (Group A) SA D & A Lab			
FRIDAY	Py Practical (Group B) MT E Sim 1		SD Practical (Group B) SA D & A Lab					VAC	
	CT Practical (Group A) NK C & N Lab								
SATURDAY									

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Principal

Paper Name	Py - Programming Fundamentals using Python	SD - Semiconductor Devices	CT - Circuit Theory and Network Analysis
Faculty	MT - Dr. Monika Tyagi	SA - Ms. Sonia Ahlawat	NK - Dr. Neha Katyal
Labs	E Sim 1 - E Simulation Lab - 1	D&A Lab - Digital & Analog Lab	C&N Lab- Circuit & Network Lab
Classes	C & N Lab - Circuit & Network Lab	Comm Lab - Communication Lab	
GE	Data Engineering and Analytics	SEC	Basic IT Tools