

# Shaheed Rajguru College of Applied Sciences for Women

**B.Sc. (H) Electronics – V semester**

**wef: 20-07-2021**

Time	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30	
Day	1	2	3	4		5	6	7	8	
<b>Monday</b>	Digital Signal Processing Mr. Deepak Jaiswal	EMT Dr. Neha Katyal	Numerical Analysis Ms. Venika Gupta	Microprocessor & Microcontrollers Ms. Preeti Singhal	L U N C H B R E A K	EMT Practical Group - B Ms. Surbhi Aggarwal		Microprocessor & Microcontrollers Practical Group - B Ms. Monika Tyagi		
						Numerical Analysis Practical Ms. Venika Gupta Group - D		EMT Practical Dr. Neha Katyal Group - D		
<b>Tuesday</b>	EMT Dr. Neha Katyal	Basic VLSI Design Dr. Manoj Kumar	Numerical Analysis Ms. Venika Gupta	Microprocessor & Microcontrollers Ms. Preeti Singhal		EMT Practical Group - B Ms. Surbhi Aggarwal				
		Digital Signal Processing Mr. Deepak Jaiswal				Microprocessor & Microcontrollers Practical Group - D Ms Preeti Singhal		Numerical Analysis Practical Group - D Ms. Sonia Ahlawat		
<b>Wednesday</b>		Basic VLSI Design Group - B Dr. Manoj Kumar	Numerical Analysis Ms. Venika Gupta	Microprocessor & Microcontrollers Ms. Preeti Singhal		Numerical Analysis Practical Group - B Ms. Venika Gupta		Basic VLSI Design Practical Group - B Dr. Manoj Kumar		
						Digital Signal Processing Practical Group - D Mr. Deepak Jaiswal				
<b>Thursday</b>		EMT Ms. Surbhi Aggarwal	Numerical Analysis Ms. Venika Gupta	Basic VLSI Design Dr. Manoj Kumar		L U N C H B R E A K	Numerical Analysis Practical Group - B Ms. Sonia Ahlawat		Basic VLSI Design Practical Group - B Dr. Manoj Kumar	
				Digital Signal Processing Mr. Deepak Jaiswal			Microprocessor & Microcontrollers Practical Group - D Ms Preeti Singhal		Digital Signal Processing Practical Group - D Mr. Deepak Jaiswal	
<b>Friday</b>		Basic VLSI Design Dr. Manoj Kumar	Microprocessor & Microcontrollers Ms. Preeti Singhal	EMT Ms. Surbhi Aggarwal			Microprocessor & Microcontrollers Practical Group - B			

No change in timetable without the permission of Principal

Dr. Punita Saxena (Convenor)

# Shaheed Rajguru College of Applied Sciences for Women

		Digital Signal Processing Mr. Deepak Jaiswal				Ms. Monika Tyagi EMT Practical Group – D Dr. Neha Katyal			

## B.Sc. (H) Electronics – III Semester

wef : 31-08-2021

Time	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30	
Day	1	2	3	4		5	6	7	8	
<b>Monday</b>	GE III	GE III	Electronic Circuit	C Programming	L U N C H B R E A K	Electronic Circuit Practical Group – 3A	C Programming Practical Group – 3A	C Programming Practical Group – 3A	C Programming Practical Group – 3A	
						C Programming Practical Group – 3B	Electronic Circuit Practical Group – 3B	C Programming Practical Group – 3A	Electronic Circuit Practical Group – 3B	
<b>Tuesday</b>	GE III	Electronic Circuit	Digital Electronics	C Programming		C Programming Practical Group – 3B	Data Science Practical Group – 3A & Group – 3B	Data Science Practical Group – 3A & Group – 3B	Digital Electronics Practical Group – 3A	Digital Electronics Practical Group – 3B
<b>Wednesday</b>	GE III	Digital Electronics Practical Group – 3A & Group – 3B		Digital Electronics						
<b>Thursday</b>	Electronic Circuit	C Programming	Data Science	Digital Electronics						
<b>Friday</b>	C Programming	Electronic Circuit	Digital Electronics	Data Science						
<b>Saturday</b>										

No change in timetable without the permission of Principal

Dr. Punita Saxena (Convenor)

# Shaheed Rajguru College of Applied Sciences for Women

**B.Sc. (H) Electronics – I Semester**

**wef: as per the university notification**

Time	9:00-10:00	10:00-11:00	11:00-12:00	12:00-1:00	1:00-1:30	1:30-2:30	2:30-3:30	3:30-4:30	4:30-5:30
Day	1	2	3	4		5	6	7	8
<b>Monday</b>			Maths FE	GE I	L U N C H B R E A K	Basic Circuit Theory & Network Analysis	AECC		
<b>Tuesday</b>		AECC	Maths FE Practical Group 1A			GE I Lab			
			Basic Circuit Theory & Network Analysis Practical Group-1B			GE I	GE I (Tut)		
<b>Wednesday</b>	Basic Circuit Theory & Network Analysis	GE I	Maths FE Practical Group 1B			Maths FE			
			Basic Circuit Theory & Network Analysis Practical Group-1A						
<b>Thursday</b>	Basic Circuit Theory & Network Analysis	GE I	Maths FE Practical Group 1A & Group 1B			Maths FE	AECC		
<b>Friday</b>	GE I	Maths FE	Basic Circuit Theory & Network Analysis Practical Group 1A & Group 1B			Basic Circuit Theory & Network Analysis			
<b>Saturday</b>			AECC						

No change in timetable without the permission of Principal

Dr. Punita Saxena (Convenor)