

# Shaheed Rajguru College of Applied Sciences for Women

Course: B.Sc. (H) Electronics (V Semester)

20.07.2022

Time	09:00 – 10:00	10:00 – 11:00	11:00 – 12:00	12:00 – 01:00	01:00 – 01:30	01:30 – 02:30	02:30 – 03:30	03:30 – 04:30	04:30 – 05:30
Days	1	2	3	4		5	6	7	8
Monday	Microprocessor Dr Monika Tyagi LT – 21	Electromagnetics Practical (Group – 5A) Ms. Surbhi Aggarwal Sim Lab 2		Numerical Analysis Ms. Venika Gupta LT – 21	L U N C H  B R E A K	Microprocessor Practical (Group – 5A) Mr. Deepak Jaiswal Communication Lab			
		Microprocessor Practical (Group – 5B) Mr. Deepak Jaiswal Communication Lab				Numerical Analysis (Group – 5B) Ms. Venika Gupta Sim Lab 2			
Tuesday	Microprocessor Dr Monika Tyagi LT – 21	Electromagnetics Dr Neha Katyal LT – 21	Basic VLSI Design Dr Manoj Kumar LT – 21	Numerical Analysis Ms. Venika Gupta LT – 21		Electromagnetics Practical (Group – 5B) Ms. Surbhi Aggarwal Sim Lab 2			
Wednesday	Microprocessor Dr Monika Tyagi LT – 21	Basic VLSI Design Dr Manoj Kumar LT – 21	Numerical Analysis Ms. Venika Gupta LT – 21	Electromagnetics Ms Surbhi Aggarwal LT – 21		Basic VLSI Design Practical Dr Manoj Kumar Sim Lab 1			
Thursday	Electromagnetics Practical Ms. Surbhi Aggarwal & Dr Neha Katyal Sim Lab 2		Basic VLSI Design Dr Manoj Kumar LT – 21	Numerical Analysis Ms. Venika Gupta LT – 21		Numerical Analysis Practical (Group – 5A) Ms. Venika Gupta Sim Lab 2		Basic VLSI Design Practical Dr Manoj Kumar Sim Lab 1	
						Microprocessor Practical (Group – 5B) Mr. Deepak Jaiswal Communication Lab			
Friday	Electromagnetics Dr Neha Katyal LT – 21	Microprocessor Dr Monika Tyagi LT – 21	Basic VLSI Design Dr Manoj Kumar LT – 21	Electromagnetics Ms Surbhi Aggarwal LT – 21		Microprocessor Practical (Group – 5A) Mr. Deepak Jaiswal Communication Lab		Numerical Analysis Practical (Group – 5A) Ms. Venika Gupta Sim Lab 2	
						Numerical Analysis Practical (Group – 5B) Ms. Venika Gupta Sim Lab 2			

No change in timetable without the permission of Principal

Dr Punita Saxena (convenor)

# Shaheed Rajguru College of Applied Sciences for Women

Course: B.Sc. (H) Electronics (III Semester)

20.07.2022

Time	09:00 – 10:00	10:00 – 11:00	11:00 – 12:00	12:00 – 01:00	01:00 – 01:30	01:30 – 02:30	02:30 – 03:30	03:30 – 04:30	04:30 – 05:30
Days	1	2	3	4		5	6	7	8
Monday	GE3	GE3	C Programming and Data Structures Dr Neha Katyal Digital and Analog Lab	Electronic Circuits Ms. Sonia Ahlawat LT – 21	L U N C H  B R E A K	Digital Electronics and Verilog/VHDL Practical (Group – 3A) Dr Neha Katyal Digital and Analog Lab		Electronic Circuits Practical Ms. Sonia Ahlawat Digital and Analog Lab	
						C Programming and Data Structures Practical (Group – 3B) Dr Monika Tyagi Sim Lab 1			
Tuesday	GE3	Electronic Circuits Ms. Sonia Ahlawat Circuit and Networks Lab	Digital Electronics Mr Deepak Jaiswal Circuit and Networks Lab	C Programming and Data Structures Dr Neha Katyal Circuit and Networks Lab		C Programming and Data Structures Practical (Group – 3A) Dr Monika Tyagi Sim Lab 1		Internet and JAVA Programming Practical Ms. Surbhi Aggarwal Sim Lab 2	
						Digital Electronics and Verilog/VHDL Practical (Group – 3B) Dr Neha Katyal Digital and Analog Lab			
Wednesday	GE3	Digital Electronics Mr Deepak Jaiswal Circuit and Networks Lab	Internet and JAVA Programming Practical Ms. Surbhi Aggarwal Circuit and Networks Lab	C Programming and Data Structures Dr Neha Katyal Circuit and Networks Lab		Electronic Circuits Practical Ms. Sonia Ahlawat Digital and Analog Lab		Digital Electronics and Verilog/VHDL Practical (Group – 3A) Dr Neha Katyal Digital and Analog Lab	
								Internet and JAVA Programming Practical (Group – 3B) Ms. Surbhi Aggarwal Sim Lab 2	
Thursday	Electronic Circuits	Digital Electronics	Internet and JAVA Programming Practical (Group – 3A)			C Programming and Data Structures Practical (Group – 3A)			

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	Ms. Sonia Ahlawat LT – 21	Mr Deepak Jaiswal LT – 21	Ms. Surbhi Aggarwal Sim Lab 2			Dr Monika Tyagi Sim Lab 1	
			C Programming and Data Structures Practical (Group – 3B) Ms Surbhi Aggarwal Sim Lab 1			Digital Electronics and Verilog/VHDL Practical (Group – 3B) Dr Neha Katyal Digital and Analog Lab	
<b>Friday</b>	Electronic Circuits Ms. Sonia Ahlawat Circuit and Networks Lab	C Programming and Data Structures Dr Neha Katyal Circuit and Networks Lab	Internet and JAVA Programming Practical Ms. Surbhi Aggarwal Circuit and Networks Lab	Digital Electronics Mr Deepak Jaiswal Circuit and Networks Lab		GE Practical	

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# Shaheed Rajguru College of Applied Sciences for Women

Course: B.Sc. (H) Electronics (I Semester)

20.07.2022

Time	09:00 – 10:00	10:00 – 11:00	11:00 – 12:00	12:00 – 01:00	01:00 – 01:30	01:30 – 02:30	02:30 – 03:30	03:30 – 04:30	04:30 – 05:30
Days	1	2	3	4		5	6	7	8
Monday		Programming Fundamentals using Python Dr Monika Tyagi Circuit and Networks Lab	Semiconductor Devices Ms. Sonia Ahlawat Circuit and Networks Lab		L U N C H  B R E A K	AECC	AECC	VAC	VAC
Tuesday		Circuit Theory and Network Analysis Mr. Deepak Jaiswal Digital and Analog Lab	Programming Fundamentals using Python Practical (Group – 1A) Dr Monika Tyagi Sim Lab 1			GE Practical			
			Semiconductor Devices Practical (Group – 1B) Ms. Sonia Ahlawat Digital and Analog Lab			SEC Practical			
Wednesday		Programming Fundamentals using Python Dr Monika Tyagi Digital and Analog Lab	Circuit Theory and Network Analysis Mr. Deepak Jaiswal Digital and Analog Lab	Semiconductor Devices Ms. Sonia Ahlawat Digital and Analog Lab					
Thursday	GE 1	GE 1	Circuit Theory and Network Analysis Mr. Deepak Jaiswal Circuit and Networks Lab	Programming Fundamentals using Python Dr Monika Tyagi Circuit and Networks Lab					
Friday		Semiconductor Devices	Semiconductor Devices Practical (Group – 1A) Ms. Sonia Ahlawat			Circuit Theory and Network Analysis Practical Mr. Deepak Jaiswal			

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		Ms. Sonia Ahlawat Digital and Analog Lab	Digital and Analog Lab				Circuit and Networks Lab
			Programming Fundamentals using Python Practical (Group – 1B) Dr Monika Tyagi Sim Lab 1				

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