Course: B.Sc. (H) Electronics (V Semester)

20.07.2022

Time	09:00 - 10:00	10:00 - 11:00	11:00 - 12:00	12:00 - 01:00	01:00 - 01:30	01:30 - 02:30	02:30 - 03:30	03:30 - 04:30	04:30 - 05:30
Days	1	2	3	4		5	6	7	8
Monday	Microprocessor Dr Monika Tyagi LT – 21	Electromagnetics Practical (Group – 5A) Ms. Surbhi Aggarwal Sim Lab 2		Numerical Analysis Ms. Venika Gupta LT – 21		Microprocessor Practical (Group – 5A) Mr. Deepak Jaiswal Communication Lab			
		(Grou Mr. Deep	ssor Practical ap – 5B) oak Jaiswal ication Lab		L	Numerical Analysis (Group – 5B) Ms. Venika Gupta Sim Lab 2			
Tuesday	Microprocessor Dr Monika Tyagi LT – 21	Electromagnetics Dr Neha Katyal LT – 21	Basic VLSI Design Dr Manoj Kumar LT – 21	Numerical Analysis Ms. Venika Gupta LT – 21	U N C	Electromagnetics Practical (Group – 5B) Ms. Surbhi Aggarwal Sim Lab 2			
Wednesda y	Microprocessor Dr Monika Tyagi LT – 21	Basic VLSI Design Dr Manoj Kumar LT – 21	Numerical Analysis Ms. Venika Gupta LT – 21	Electromagnetics Ms Surbhi Aggarwal LT – 21	H B	Basic VLSI De Dr Mano Sim I	j Kumar		
Thursday	Electromagnetics Practical Ms. Surbhi Aggarwal & Dr Neha Katyal Sim Lab 2		Basic VLSI Design Dr Manoj Kumar LT – 21	Numerical Analysis Ms. Venika Gupta LT – 21	R E A K	Numerical Ana (Group Ms. Venil Sim I Microprocess (Group Mr. Deepa Communic	-5A) ka Gupta ab 2 sor Practical -5B) k Jaiswal	Dr Mano	esign Practical oj Kumar Lab 1
Friday	Electromagnetics Dr Neha Katyal LT – 21 Dr Monika Tyagi LT – 21		Basic VLSI Design Dr Manoj Kumar LT – 21	Electromagnetics Ms Surbhi Aggarwal LT – 21		Microprocess (Group Mr. Deepa Communic Numerical Ana (Group Ms. Venil	- 5A) k Jaiswal cation Lab lysis Practical - 5B) ka Gupta	Numerical Analysis Practice (Group – 5A) Ms. Venika Gupta Sim Lab 2	

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Dr Punita Saxena (convenor)

Course: B.Sc. (H) Electronics (III Semester)

20.07.2022

Time	09:00 - 10:00	10:00 - 11:00	11:00 - 12:00	12:00 - 01:00	01:00 - 01:30	01:30 - 02:30	02:30 - 03:30	03:30 - 04:30	04:30 - 05:30
Days	1	2	3	4		5	6	7	8
Monday	GE3	Electronic Circuits Ms. Sonia Ahlawat Circuit and Networks Lab	C Programming and Data Structures Dr Neha Katyal Digital and Analog Lab Digital Electronics Mr Deepak Jaiswal Circuit and Networks Lab	Electronic Circuits Ms. Sonia Ahlawat LT – 21 C Programming and Data Structures Dr Neha Katyal Circuit and Networks Lab	L U N C H B R E	Verilog/V (Gre Dr N Digital a C Programming P (Gre Dr Me Si C Programming P (Gre Dr Me Si Ugrital F Verilog/V (Gre Dr N	Clectronics and CHDL Practical Dup – 3A) eha Katyal and Analog Lab g and Data Structures ractical Dup – 3B) Donika Tyagi m Lab 1 g and Data Structures ractical Dup – 3A) Donika Tyagi m Lab 1 Clectronics and CHDL Practical Dup – 3B) eha Katyal and Analog Lab	Electronic Circ Ms. Sonia Digital and A Internet and JAV Pract Ms. Surbhi Sim L	Ahlawat Analog Lab A Programming ical Aggarwal
Wednesday	GE3	Digital Electronics Mr Deepak Jaiswal Circuit and Networks Lab	Internet and JAVA Programming Practical Ms. Surbhi Aggarwal Circuit and Networks Lab	C Programming and Data Structures Dr Neha Katyal Circuit and Networks Lab	K	Electronic Circuits Practical Ms. Sonia Ahlawat Digital and Analog Lab		Digital Elect Verilog/VHD (Group Dr Neha Digital and A Internet and JAV Pract (Group Ms. Surbhi Sim L	A Programming ical A 3B) A Programming ical A 3B) Aggarwal
Thursday	Electronic Circuits	Digital Electronics	Pr	AVA Programming ractical up – 3A)		C Programming and Data Structures Practical (Group – 3A)			

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Dr Punita Saxena (convenor)

	Ms. Sonia Ahlawat	Mr Deepak Jaiswal	Ms. Surbhi Aggarwal Sim Lab 2		Dr Monika Tyagi Sim Lab 1	
	LT – 21 C Programming and Data Structures Practical (Group – 3B) Ms Surbhi Aggarwal		Digital Electronics and Verilog/VHDL Practical (Group – 3B) Dr Neha Katyal			
				n Lab 1	Digital and Analog Lab	
Friday	Electronic	C Programming	Internet and	Digital Electronics	GE	Practical
	Circuits	and Data	JAVA	Mr Deepak Jaiswal		
	Ms. Sonia	Structures	Programming	Circuit and		
	Ahlawat	Dr Neha Katyal	Practical	Networks Lab		
	Circuit and	Circuit and	Ms. Surbhi			
	Networks Lab	Networks Lab	Aggarwal			
			Circuit and			
			Networks Lab			

Course: B.Sc. (H) Electronics (I Semester)

20.07.2022

Time	09:00 - 10:00	10:00 - 11:00	11:00 - 12:00	12:00 - 01:00	01:00 - 01:30	01:30 - 02:30	02:30 - 03:30	03:30 - 04:30	04:30 - 05:30
Days	1	2	3	4		5	6	7	8
Monday		Programming Fundamentals using Python Dr Monika Tyagi Circuit and Networks Lab	Semiconductor Devices Ms. Sonia Ahlawat Circuit and Networks Lab			AECC	AECC	VAC	VAC
Tuesday		Circuit Theory and Network Analysis Mr. Deepak Jaiswal Digital and Analog Lab	Programming Fundamentals using Python Practical (Group – 1A) Dr Monika Tyagi Sim Lab 1 Semiconductor Devices Practical (Group – 1B) Ms. Sonia Ahlawat Digital and Analog Lab			GE Practical			
Wednesday		Programming Fundamentals using Python Dr Monika Tyagi Digital and Analog Lab	Circuit Theory and Network Analysis Mr. Deepak Jaiswal Digital and Analog Lab	Semiconductor Devices Ms. Sonia Ahlawat Digital and Analog Lab	B R E A K	SEC Practical			
Thursday	GE 1	GE 1	Circuit Theory and Network Analysis Mr. Deepak Jaiswal Circuit and Networks Lab	Programming Fundamentals using Python Dr Monika Tyagi Circuit and Networks Lab					
Friday		Semiconductor Devices	Semiconductor E (Group Ms. Sonia	-1A)				Pra	d Network Analysis ctical oak Jaiswal

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Ms. Sonia	Digital and Analog Lab		Circuit and Networks Lab
Ahlawat			
Digital and			
Analog Lab	Programming Fundamentals using		
	Python Practical		
	(Group – 1B)		
	Dr Monika Tyagi		
	Sim Lab 1		