DISCIPLINE SPECIFIC ELECTIVES (DSE) COURSES OFFERED BY THE DEPARTMENT

CREDIT DISTRIBUTION, ELIGIBILITY AND PRE-REQUISITES OF THE COURSE

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course (if any)
		Lecture	Tutorial	Practical/ Practice		
Computer Networks	4	3	-	1	Class XII passed with Physics + Mathematics/Applied Mathematics + Chemistry OR Physics + Mathematics/Applied Mathematics + Computer Science/Informatics Practices	Programming Language (DSC 1, Sem I)/ Algorithm Design and Analysis(DSE 1B, Sem III), Operating System(DSE 2B, Sem IV)

Learning Objectives

The course objectives include learning about computer network organization and implementation, obtaining a theoretical understanding of data communication and computer networks, and gaining practical experience. This course introduces the student to the fundamental understanding of the architecture and principles of today's computer networks. It introduces various protocols and their functionalities. This course will help to understand The Internet and its impact on the computer network architecture.

Learning outcomes

The Learning Outcomes of this course are as follows:

- Describing computer network in terms of a layered model.
- Implementing data link, network, and transport layer protocols in a simulated networking environment
- Determine different types of errors and data flow within networks.
- Planning logical sub-address blocks with a given address block.
- Describing the standard protocols involved with the INTERNET, TCP/IP, based communications.

SYLLABUS OF ELDSE-3A **Total Hours- Theory: 45 Hours, Practicals: 30 Hours**

UNIT – I (11 Hours)

Network Basics and Physical layer: Data Communication- Components, Network topologies, OSI Reference Model, Internet (TCP/IP) Model, Digital Signals, Digital-to-Digital Encoding, Transmission Media- Guided and Unguided, Addressing, Transmission Impairment, Nyquist Bit rate, Shannon Capacity and Line Codling Schemes, Switching-Circuit Switching, Message Switching and Packet Switching, Network Connecting Devices- Repeaters, Hubs, Switches, Bridges, Routers and Gateway.

UNIT – II (12 Hours)

Data Link Layer and MAC: Character and Bit Oriented Framing, Flow and Error Control, Error Detection and Correction Codes- Parity, Hamming Code, Cyclic Redundancy Check and Checksum, Stop and Wait Protocol, Sliding Window Protocol and Piggybacking, Go-Back-N ARQ, Selective Repeat ARQ. Random Access Protocols-ALOHA, CSMA, CSMA/CD, CSMA/CA, Controlled Access Protocols- Reservation, Token Passing and Polling, Channelization Protocols-FDMA, TDMA and CDMA.

UNIT – III (12Hours)

Network Layer: IPV4 Addresses- Classful and Classless, Subnet Addressing, NAT, Datagram Format, Internet Control Protocols- ARP, RARP and ICMP, Routing algorithms - Shortest Path and Distance Vector, Approaches to Congestion Control, IPV4 issues, Need for IPV6,IPv6 Packet Format, IPV6 Unicast and Multicast Addressing

UNIT – IV (10 Hours)

Transport and Application Layer: Transport Services, Connection management, TCP and UDP protocols, Congestion Control and Quality of Service, Application Layer-DNS, FTP, WWW and HTTP.

Practical component (if any) – Computer Networks

(The practical will need to be Simulated on Cisco Packet Tracer or an equivalent platform. All Programming experiments to be done with Python)

Learning outcomes

The Learning Outcomes of this course are as follows:

- Implement a simple network with hubs and switches.
- Understand the various LAN topologies
- Describe how packets are delivered in the Internet.
- Describe what classful addressing scheme is.
- Grasp the error detection and correction algorithms

LIST OF PRACTICALS (Total Practical Hours- 30 Hours)

1. Create a simple network with a switch and two end devices in Cisco Packet Tracer. Configure the PCs, set their IP address and capture Ping from one PC to the other and vice versa.. Mention the uses of PING command.
2. Study Network Commands: tracert, ipconfig and ipconfig/all.
3. Implement MESH/STAR/RING/BUS topology in Packet tracer.
4. Write a program to add a parity bit to a 7 bit data input by a user/ add redundant bits to a 7 bit data using Hamming Code to be implemented at the sender's site.
5. Write a program to detect and correct a single bit error while transmitting a 7-bit Hamming Code word to be implemented on the receiver side.
6. Write a program to implement CRC at the sender's site.
7. Write a program to show Byte and Bit stuffing in a frame.
8. Set a six-computer network with a switch using Packet Tracer and show Unicast and Broadcast addressing.
9. Connect two different networks using a router in Packet tracer and show movement of packets from one to the other.
10. Write a program to determine the class of the given IPV4 Address in Dotted Decimal or Binary Notation.
11. Implement FTP Server in Packet Tracer and show transfer of data.
12. Study HTTP /DNS on the Packet Tracer.

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than eleven.

Essential/recommended readings

1. Behroz A. Forouzan, " Data Communication and Networking", TMH, 5th Edition.
2. A.S.Tanenbaum, " Computer Network", Pearson Education, 4th Edition.

Suggestive readings

1. James Kurose , "Computer Networking: A Top-Down Approach", Pearson Education, 7th Edition.
2. Douglas E. Comer, "Internetworking with TCP/IP Principles, Protocol and Architecture Volume 1" , 6th Edition
3. Peterson and Davis, "Computer Networks: A Systems Approach", Pearson, 5th edition

4. Fall Kevin and W. Richard Stevens , “TCP/IP Illustrated: The Protocols” Volume 1.
5. William Stallings, “Data and Computer Communication”, Tenth Edition.

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.

DISCIPLINE SPECIFIC ELECTIVES (DSE-2)

CREDIT DISTRIBUTION, ELIGIBILITY AND PRE-REQUISITES OF THE COURSE

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course (if any)
		Lecture	Tutorial	Practical/ Practice		
Quantum and Spintronics Devices	**4**	**3**	**-**	**1**	**Class XII passed with Physics + Mathematics/Applied Mathematics + Chemistry OR Physics + Mathematics/Applied Mathematics + Computer Science/Informatics Practices**	**Semiconductor Devices(DSC 3, Sem I), Engineering Mathematics (DSC 7, Sem III)**

Learning Objectives

The objective of the course is to make the students understand the inadequacies of Classical Physics and know the basic postulates of Quantum Mechanics. Spintronics, a portmanteau meaning "spin transport electronics", where both charge and spin degrees of freedom of electrons are employed simultaneously to produce a device with new functionality, is a fascinating and promising field of research. It has the potential to revolutionize the field of electronics. Two physical bases of Spintronics, i.e., GMR and TMR have already been commercialized in read heads of the hard disk drive. It is extremely important and necessary to have a clear concept of spintronics so that students get exposure to such modern-day cutting-edge technology. Students will also learn general concepts about Spin-based quantum computing which is a leading technology for the realization of scalable quantum computers and other sectors too.

Learning outcomes

The Learning Outcomes of this course are as follows:

- Understand the limitation of classical physics and basic concepts of quantum Mechanics
- Understanding the concept of spintronics and spin-orbit
- Comprehend the spin relaxation and transport
- Design the spintronics devices using the laws
- Know the basic principles of various spintronic devices (sensors, memories, etc.)

SYLLABUS OF ELDSE-3B **Total Hours- Theory: 45 Hours, Practicals: 30 Hours**

UNIT – I (11 Hours)

Introduction to Quantum Mechanics: Inadequacies of Classical physics, Wave-particle duality, de Broglie waves, Schrödinger equation, expectation values, Uncertainty principle.

Basics of Quantum Mechanics: Solutions of the one-dimensional Schrödinger equation for a free particle, particle in a box, particle in a finite well. Reflection and transmission by a potential step and by a rectangular barrier. Basic understating of the Linear algebra of quantum computing.

UNIT – II (12 Hours)

History & Background of spintronics : GMR, Datta-Das, Spin relaxation, Spin injection, Spin detection

Electron Spin in Solids: Quantum Mechanics of spins, Pauli equation, Spin-Orbit coupling, Zeeman splitting, Current density, Magnetization, Bloch states with SO coupling, Electronic structure of GaAs, Dresselhaus and Rashba spin splitting, Optical orientation and spin pumping, Stern-Gerlach experiments with electron spins, Detection of free electron spin

UNIT – III (11 Hours)

Transport in magnetic materials and Spin injection: Materials for spin electronics, Nanostructures for spin electronics, Spin-polarized transport, Electrochemical potential, Spin accumulation, Spin diffusion, FN junction, Rashba formalism of linear spin injection, Equivalent circuit model, Silsbee-Johnson spin-charge coupling

UNIT – IV (11 Hours)

Spintronic Devices: Datta-Das spin-FET, P-N junctions, Magnetic bipolar diode, Magnetic bipolar transistor, Magnetic tunneling devices, MRAM, New memory technologies

Practical component (if any) – Quantum and Spintronics Devices
Hardware and Simulation-Based Lab Experiments
(Scilab/MATLAB/SPICE/Verilog A)

Learning outcomes

The Learning Outcomes of this course are as follows:

- Perform lab experiment on splitting of atomic energy levels under magnetic field by Zeeman Effect
- Perform simulations to under spin phenomenon using transport and magnetic elemental modules using Scilab/MATLAB/SPICE/Verilog A

- Extending use of elemental modules to build Spin Circuit Models for complex structures

LIST OF PRACTICALS (Total Practical Hours – 30 Hours)

1. Study of Zeeman Effect

Simulation using Transport and Magnetic Elemental Modules to understand Spin Phenomenon and build Spin Circuit Models using Scilab/MATLAB/SPICE/Verilog A (https://nanohub.org/groups/spintronics) for the following

2. Non Magnet
3. Ferromagnet
4. Magnetic Tunnel Junction
5. Rashba Spin Orbital
6. Giant Spin Hall Effect
7. Spin Pumping
8. Pure Spin Conductor
9. Magnetic Coupling

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than eight.

Essential/recommended readings

1. Beiser, Concepts of Modern Physics, McGraw-Hill Book Company (1987)
2. Sadamichi Maekawa, —Concepts in Spin Electronics, Oxford University Press (2006).
3. Bandyopadhyay S, Cahay M. Introduction to Spintronics. CRC press; 2015.

Suggestive readings

1. Isaac Chuang and Michael Nielsen, Quantum Computation and Quantum Information, Cambridge University Press, 2000.
2. Supriyo Bandyopadhyay and Marc Cahay, Introduction to Spintronics, CRC press, 2008

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.

DISCIPLINE SPECIFIC ELECTIVES (DSE-3)

CREDIT DISTRIBUTION, ELIGIBILITY AND PRE-REQUISITES OF THE COURSE

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course (if any)
		Lecture	Tutorial	Practical/ Practice		
Telecommunication Switching Systems and Networks	**4**	**3**	**-**	**1**	**Class XII passed with Physics + Mathematics/Applied Mathematics + Chemistry OR Physics + Mathematics/Applied Mathematics + Computer Science/Informatics Practices**	**Principles of Communication System(DSC 12, Sem IV)**

Learning Objectives

The Learning Objectives of this course are as follows:

- To introduce and develop a conceptual understanding of telecommunication networks.
- To develop an understanding of basic traffic engineering and get familiar with the basics of modern telephone networks and data networks.

Learning outcomes

The Learning Outcomes of this course are as follows:

- Understand the basics of various Switching Systems.
- Learn in detail about Time Division Switching.
- Understand the basics of Traffic Engineering.
- Learn the fundamentals of Data Networks.
- Understand the functionality of Telephone Networks and gain familiarity with ISDN.

SYLLABUS OF ELDSE-3C Total Hours- Theory: 45 Hours, Practicals: 30 Hours

UNIT – I (11 Hours)

Introduction: Evolution of Telecommunications, Simple Telephone Communication, Manual Switching System, Major Telecommunication Networks, Strowger Switching System, Crossbar Switching.
Electronic Space Division Switching: Stored Program Control, Centralized SPC, Distributed SPC, Enhanced Services, Multi-stage Switches.

UNIT – II (12 Hours)
Time Division Switching: Time Multiplexed Space Switching, Time Multiplexed Time Switching, Combination Switching, Three-stage Combination Switching, *n*-stage Combination Switching.
Traffic Engineering: Network Traffic Load and Parameters, Grade of Service and Blocking Probability, Modelling Switching Systems, Incoming Traffic and Service Time Characterization, Introduction to Blocking Models, Loss Estimates and Delay Systems.

UNIT – III (11 Hours)
Data Networks: Block diagram, features and working of EPABX systems. Data Transmission in PSTNs, Data Rates in PSTNs, Modems, Switching Techniques for Data Transmission, Circuit Switching, Store and Forward Switching. Data Communication Architecture, ISO-OSI Reference Model, Link to Link layers, Physical Layer, Data Link Layer, Network Layer, End to End Layers, Transport Layer, Session Layer, Presentation Layer, Satellite Based Data Networks, LAN, Metropolitan Area Network, Fibre Optic Networks, and Data Network Standards.

UNIT – IV (11 Hours)
Telephone Networks and ISDN: Subscriber Loop Systems, Switching Hierarchy and Routing, Transmission Plan, Transmission Systems, Numbering Plan, Charging Plan, Signalling Techniques, Inchannel Signalling, Common Channel Signalling, Cellular Mobile Telephony.
Integrated Services Digital Networks (ISDN): ISDN services, Network and Protocol Architecture, Transmission Channels.

Practical component (if any) – Telecommunication Switching Systems and Networks ***(MATLAB/SCILAB /Any other softwares)***

Learning outcomes

The Learning Outcomes of this course are as follows:

- To learn about the various switching networks.
- To learn about traffic in the context of Telecommunication Network.
- To design and study a Local Area Network.

LIST OF PRACTICALS (Total Practical Hours – 30 Hours)

1. Simulation of Basic Switching Systems.

2. Simulation of TDMA.
3. Simulation of basic traffic parameters.
4. Simulation of PCM.
5. To study and perform TDM-PCM.
6. Study of EPABX System and its features
7. Study of LAN Trainer Kit.
8. Study of Optical Fiber Communication System.

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than seven.

Essential/recommended readings

1. Thiagarajan Viswanathan, Manav Bhatnagar, 'Telecommunication Switching Systems and Networks', Prentice Hall of India Learning Pvt. Ltd., 2015
2. J. E Flood, 'Telecommunications Switching, Traffic and Networks', Pearson Education, 2006
3. John C Bellamy, Digital Telephony, John Wiley International Student Edition, 3rd Edition, 2000
4. Tomasi, Introduction to Data Communication and Networking, Pearson Education, 1st Edition, 2007

Suggestive readings

1. Behrouz A. Forouzan, Data Communications and Networking, TMH, 2nd Edition, 2002

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.